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5-23-01
WZ

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

$$\begin{array}{ccccccc} \mathbb{Z}^{n_1} & \mathbb{Z}^{n_2} & \mathbb{Z}^{n_3} & \mathbb{Z}^{n_4} & \mathbb{Z}^{n_5} & \mathbb{Z}^{n_6} & \mathbb{Z}^{n_7} \\ \downarrow & \downarrow & \downarrow & \downarrow & \downarrow & \downarrow & \downarrow \\ \mathbb{Z}^{n_1} & \mathbb{Z}^{n_2} & \mathbb{Z}^{n_3} & \mathbb{Z}^{n_4} & \mathbb{Z}^{n_5} & \mathbb{Z}^{n_6} & \mathbb{Z}^{n_7} \end{array}$$

Sir:

IN THE CLAIMS

Claim 9, Line 1 Please delete "or claim 8".

Claim 13, Line 2 Please change "any of claims 6 to 10"
to --claim 6--.

Please add the following new claims:

Cont.

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-- a graphics engine for providing graphics signals to said
graphics channel and a first I2C communication channel complying
with the DDC/CI protocol for controlling said at least one
display;
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-- arbitration means having a first input connected to receive
said first I2C communication channel provided by said graphics
engine, and having a second input receiving a second I2C
communication channel provided by said hardware monitoring
circuit;
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-- said arbitration means for providing between said first and
 said second I²C communication channels so that both hardware
 monitoring circuit and said processor can get access to said
 second receiving means of said at least one display without
 contention.